

CLAIMS:

What is claimed is:

1. A device comprising:
an active region;
a gated device within the active region; and
a contact in the active region to a gate of the gated device.
2. The device of claim 1, wherein the gated device comprises at least one SRAM cell.
3. The device of claim 1, wherein the contact is a first contact and the active region further comprises:
a dielectric layer on the gated device; and
a second contact through the dielectric layer to a junction region of the gated device.
4. The device of claim 3, wherein the dielectric layer comprises a material selected from the group consisting SiO_2 , PSG, Si_3N_4 , and SiC.
5. The device of claim 3, wherein the dielectric layer on the gated device further comprises a conformal etch stop layer on the junction region and the gate of the gated device.
6. The device of claim 5, wherein the conformal etch stop layer comprises a material selected from the group consisting SiO_2 , PSG, Si_3N_4 , and SiC.
7. The device of claim 5, wherein the second contact comprises:
a third contact through the dielectric layer to the conformal etch stop layer; and
a fourth contact through the conformal etch stop layer to the gate.
8. The device of claim 5, wherein the dielectric layer on the gated device further comprises:
a planarized first dielectric layer in the active region of the gated device exposing the conformal etch stop layer on a portion of the gate;

a different second dielectric layer on the gated device in the active region; and

wherein the second contact through the dielectric layer and to the gate further comprises:

- a third gate contact to the different second dielectric layer formed using an etch chemistry having a greater selectivity for the second dielectric layer than for the first dielectric layer and etch stop layer; and
- a fourth gate contact through the etch stop layer to the gate.

9. The device of claim 5, wherein the dielectric layer on the gated device further comprises:

a planarized first dielectric layer in the active region of the gated device for exposing the conformal etch stop layer on a portion of the gate;

a second etch stop layer on the first dielectric layer and the conformal etch stop layer;

a second dielectric layer on the second etch stop layer; and

wherein the second contact through the dielectric layer and to the gate further comprises:

- a third gate contact to the second etch stop layer; and
- a fourth gate contact to the gate.

10. The device of claim 3, wherein the dielectric layer on the gated device further comprises:

a first partially planarized conformal dielectric layer on the gated device in the active region having a first height on a portion of the junction region which is less than the height of said gate, and having a different second height on a portion of the gate which is substantially less than the first height;

a different second dielectric layer on the gated device in the active region; and

wherein the second contact through the dielectric layer and to the gate further comprises:

- a third contact through the different second dielectric layer to the first dielectric layer;
- a fourth contact through the first dielectric layer to the gate.